

## Patent Abstracts of Japan

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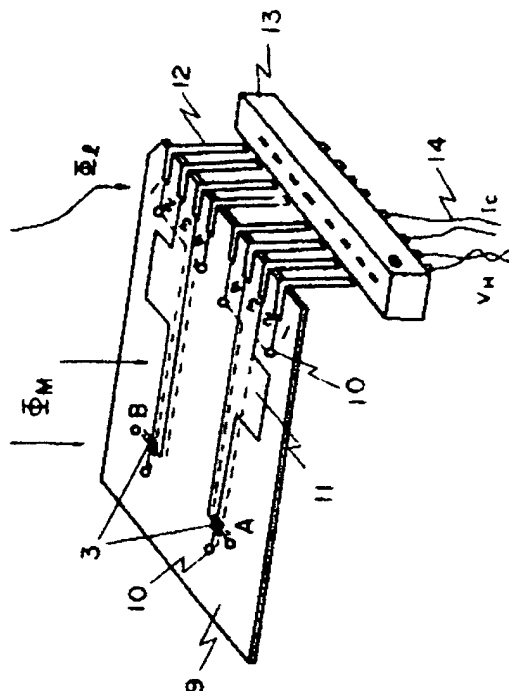
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TITLE : CURRENT TRANSFORMER



ABSTRACT : PURPOSE: To decrease currents to be measured by a method wherein two semiconductor magneto-sensitive elements are disposed to a printed wiring insulating substrate, induction electromotive force is minimized in the drawing direction of printed wiring and a connecting pin and the connecting pin is symmetrized with an element drawing line.

CONSTITUTION: The two Hall elements 3 are mounted onto the insulating substrate 9 such as a ceramic board, and pairs of each lead wire from the Hall elements 3 are printed and wired so as to be mutually insulated in the magnetic flux passing direction and stack approximately. The connecting pins 12 are attached at an end section of the printed wiring, and a loop 11 is formed in order to deny induction electromotive force due to the drawing of the connecting pins 12. The connecting pins 12 are connected to external connecting lines 14 by means of a connector 13, but bent as shown in the figure in order to prevent the generation of the induction electromotive force of the connecting sections by the connector 13. Accordingly, a substrate unit is extremely thinned by printed wiring and can correspond to low currents, a plurality of the Hall elements can equally be distributed, and characteristics such as the reduction of the effects of external magnetic fields can be improved.

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